Doc: X3T10.1/95a197ro

February 15, 1996 Project: 1146 Ref Doc.: PH2

Reply to: Lisa A. Huff

To: X3T10.1 Membership

From: Lisa A. Huff, Development Engineer - AMP Incorporated

Subject: AMP High Speed Serial Data Connector (HSSDC) Inclusion in X3T10.1/1146 (PH2)

## **BACKGROUND**

The HSSDC is a connector which was specifically designed for high speed data transmission. It is impedance matched for a 150 ohm differential system. As bit rates increase, even slight impedance mismatches will cause multiple reflections that will degrade the signal enough to cause high bit error rates

## **PROPOSAL**

Include the HSSDC in the PH2 specification (Paragraph 8.5 in PH1) as an alternative to the Shielded Micro D for external connections. The HSSDC will meet the current PH1 impedance specification of 150  $\pm$  10 $\Omega$  at rise times as fast as 150 ps. Figure 1 shows the impedance profile for the ITT Cannon Micro D and the AMP HSSDC at several different rise times. This data was taken utilizing the Tektronix 11801B Oscilloscope in TDR mode. It is apparent that the ITT Cannon Micro D failed the impedance requirement at a rise time of 500 ps or less. The AMP HSSDC failed the impedance requirement at 100 ps rise time.

The maximum length of unequalized cable assemblies made with the HSSDC Plugs running at 20 Mbytes/s is 40 meters. The maximum length of unequalized cable assemblies made with the HSSDC Plugs running at 40 Mbytes/s is 40 meters (if the receiver eye pattern amplitude is half that at 20 Mbytes/s). Eye pattern data (Figures 2-11) for unequalized cable assemblies is shown below. The data was taken with the Tektronix 11801B Oscilloscope and the HP 8133A Pulse Generator.

Adding equalization to the cable assemblies could effectively double the maximum length. In order to increase the maximum cable assembly length, new cable designs and equalization circuits are under development.

Figures 12 and 13 show the cable assembly interface. Figure 14 is the cable assembly.

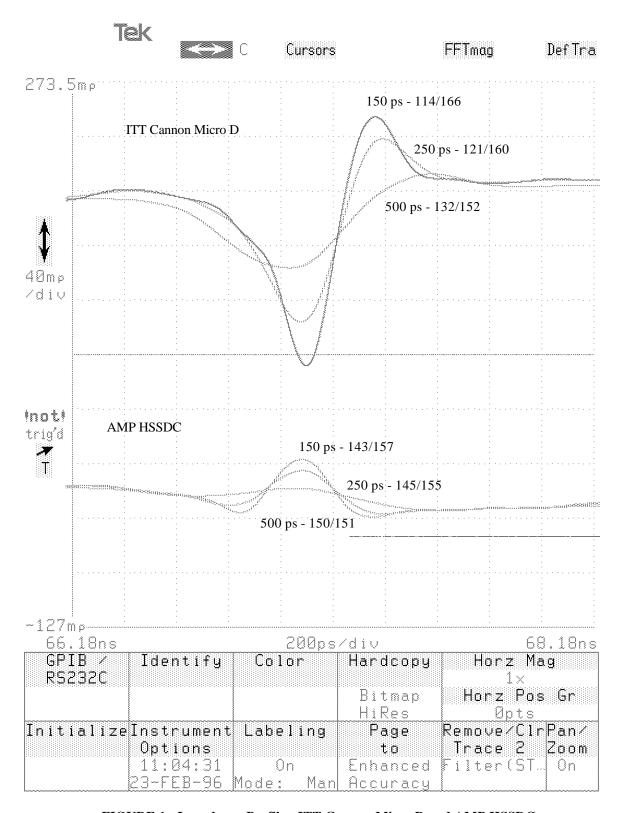


FIGURE 1: Impedance Profile - ITT Cannon Micro D and AMP HSSDC

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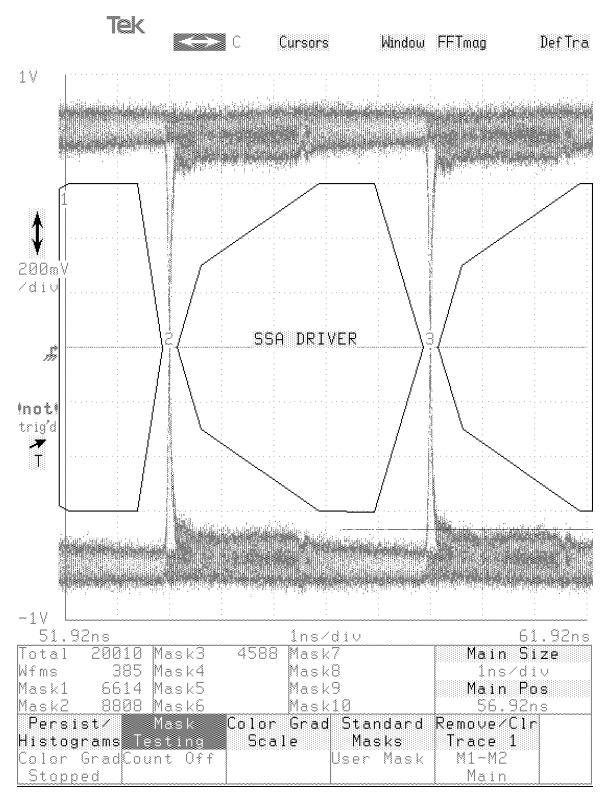


Figure 2: Input Eye Pattern 200 Mbps

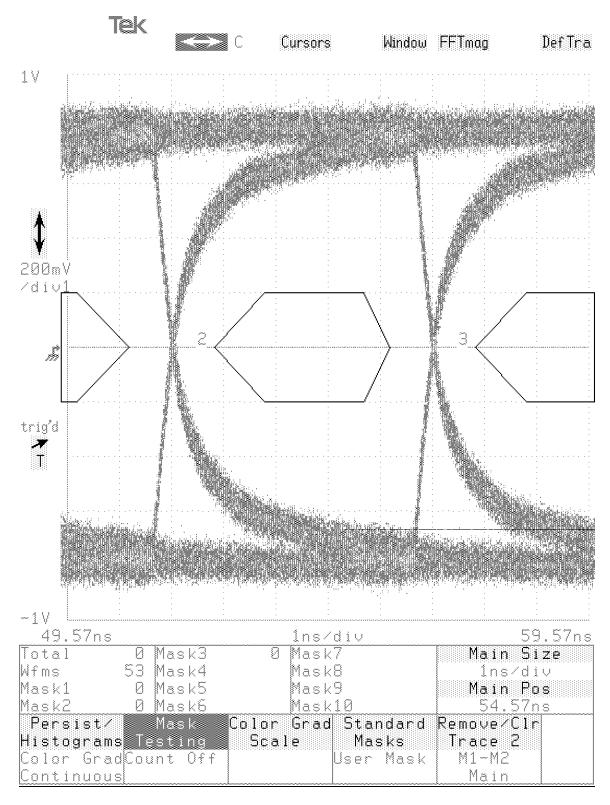


Figure 3: 200Mbps, 10m, 28AWG Shielded Quad

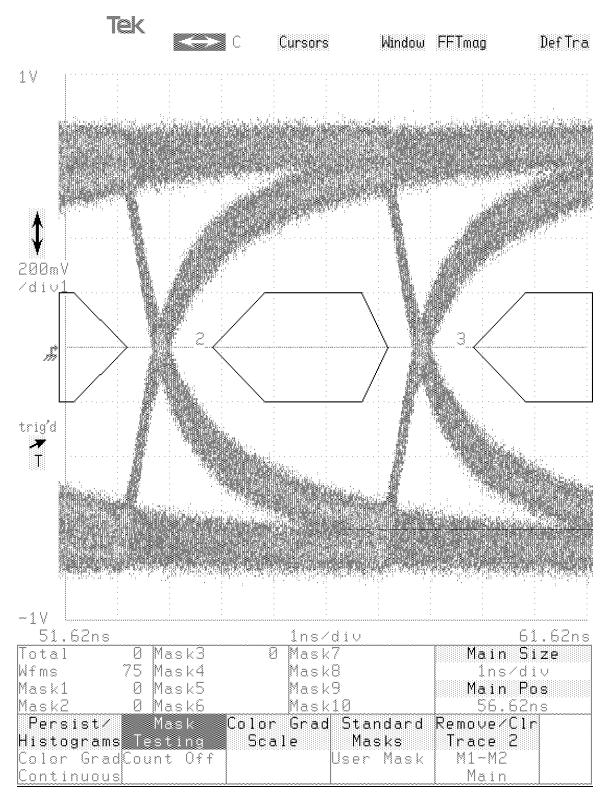


Figure 4: 200 Mbps, 20m, 28 AWG Shielded Quad

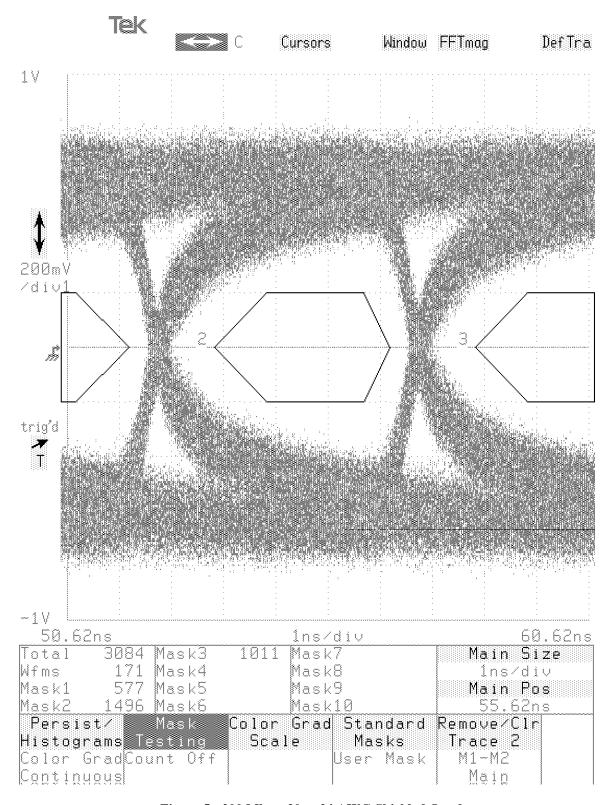


Figure 5: 200 Mbps, 30m, 24 AWG Shielded Quad

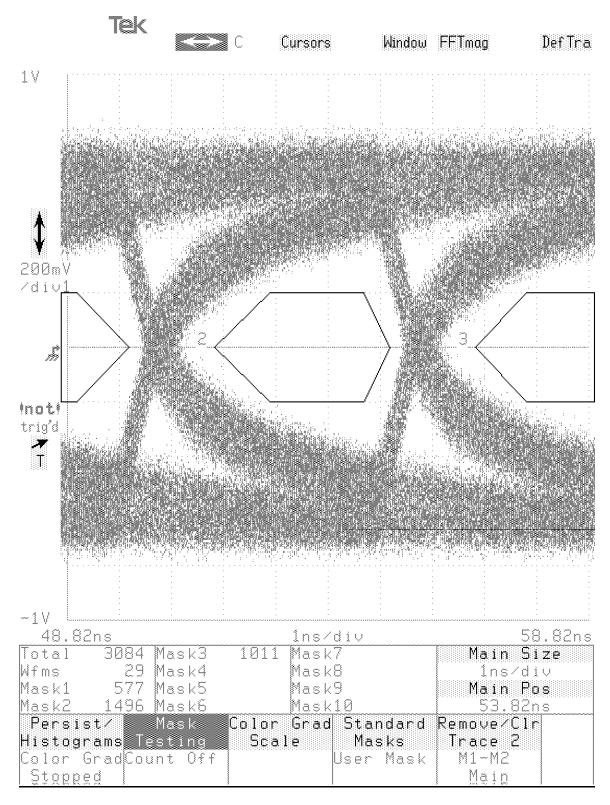


Figure 6: 200Mbps, 40m, 24 AWG Shielded Quad

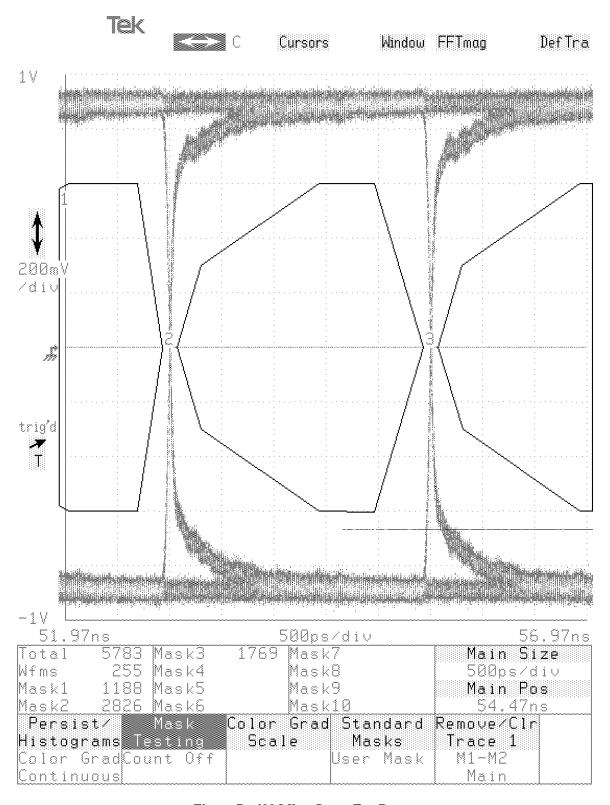


Figure 7: 400 Mbps Input Eye Pattern

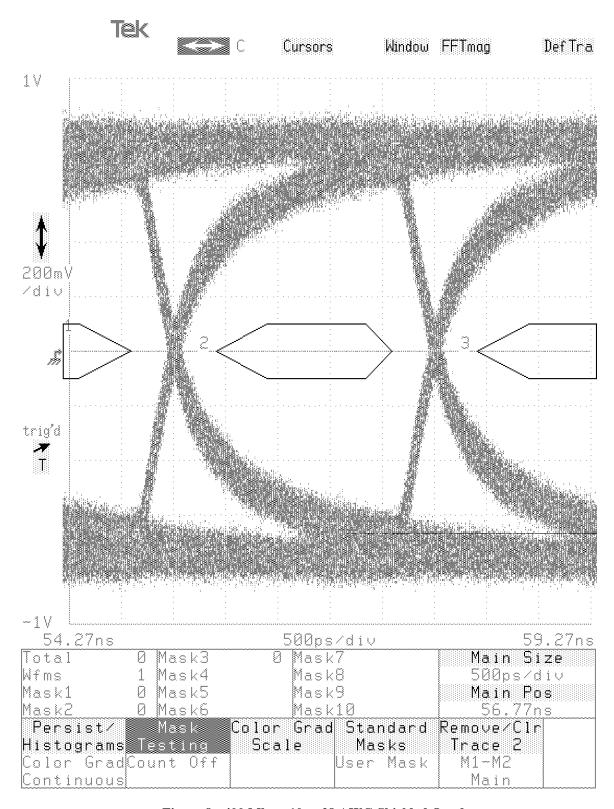


Figure 8: 400 Mbps, 10m, 28 AWG Shielded Quad

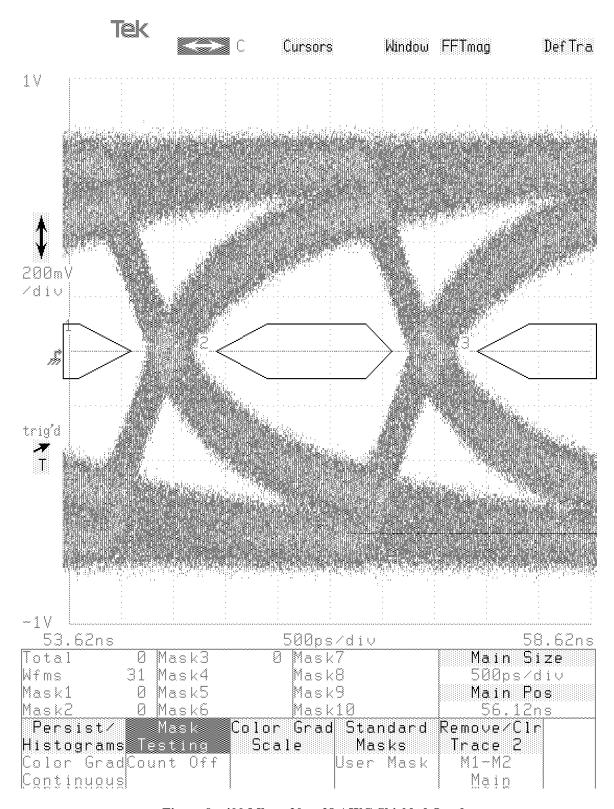


Figure 9: 400 Mbps, 20m, 28 AWG Shielded Quad

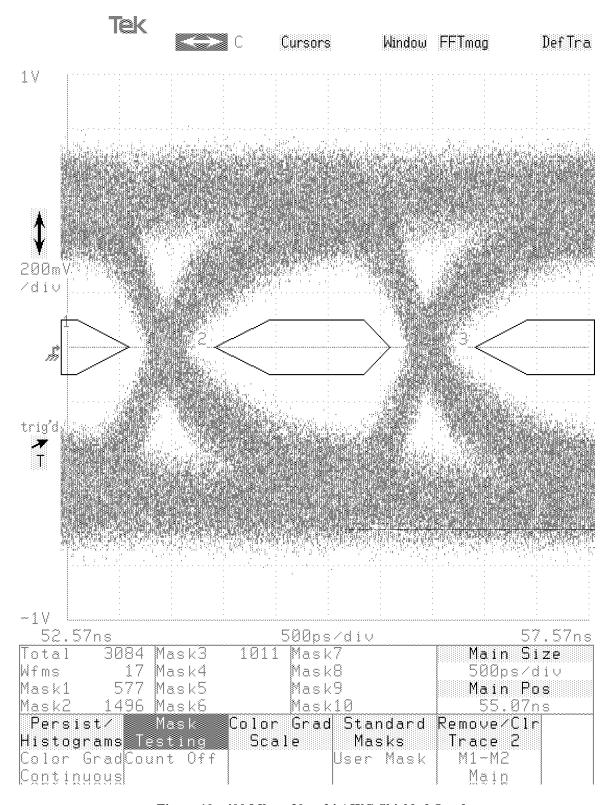


Figure 10: 400 Mbps, 30m, 24 AWG Shielded Quad

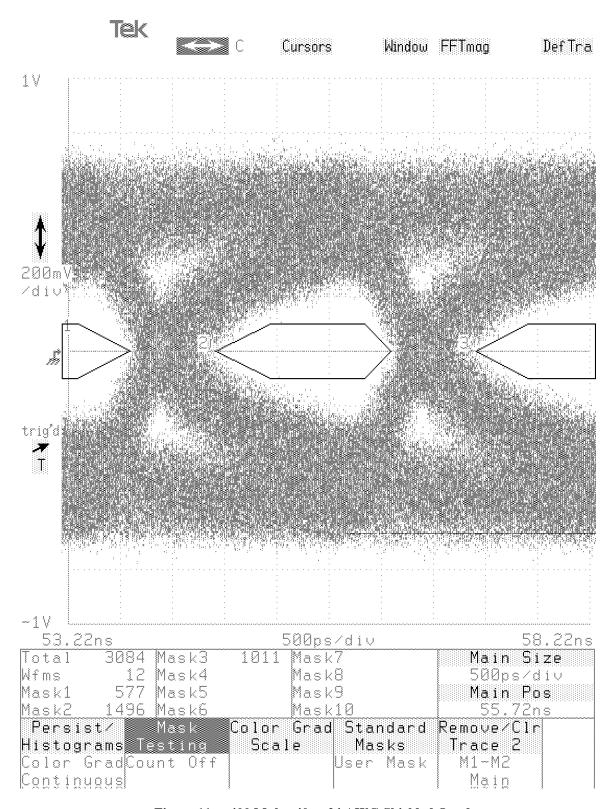


Figure 11: 400 Mpbs, 40m, 24 AWG Shielded Quad

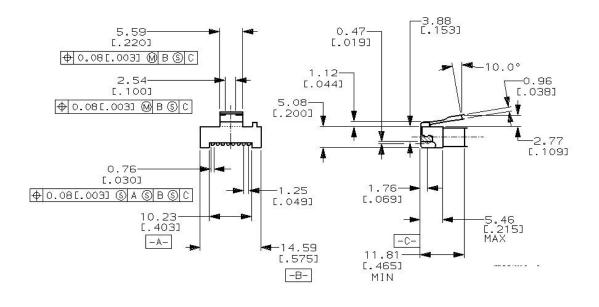


Figure 12: Interface HSSDC Cable Assembly

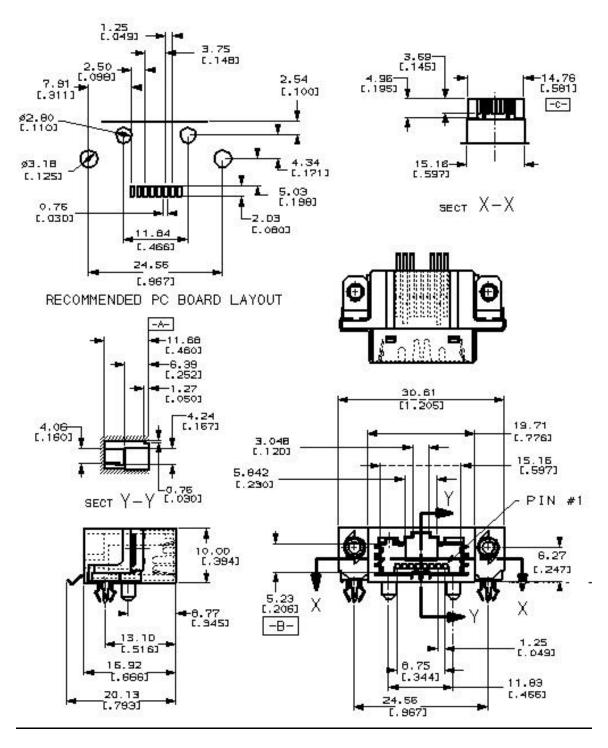
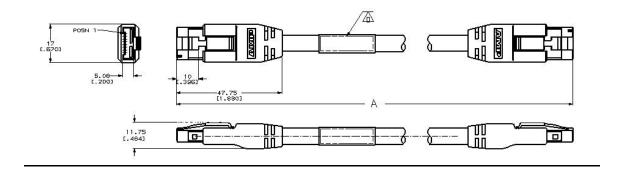


Figure 13: Interface HSSDC PCB Connector



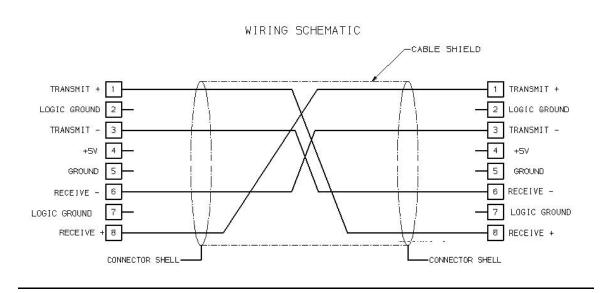


Figure 14: HSSDC Cable Assembly